

FIG. 1

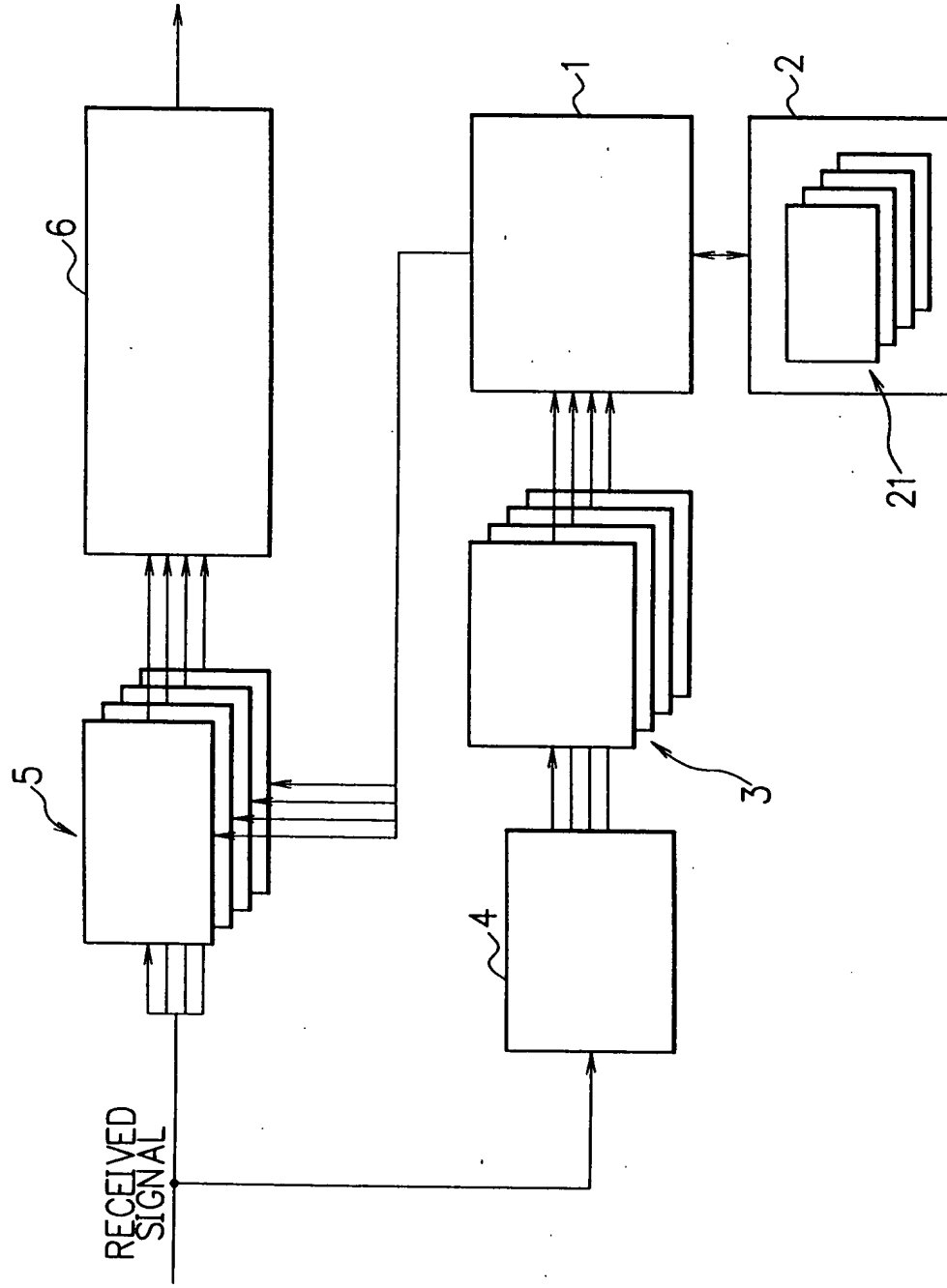


FIG. 2

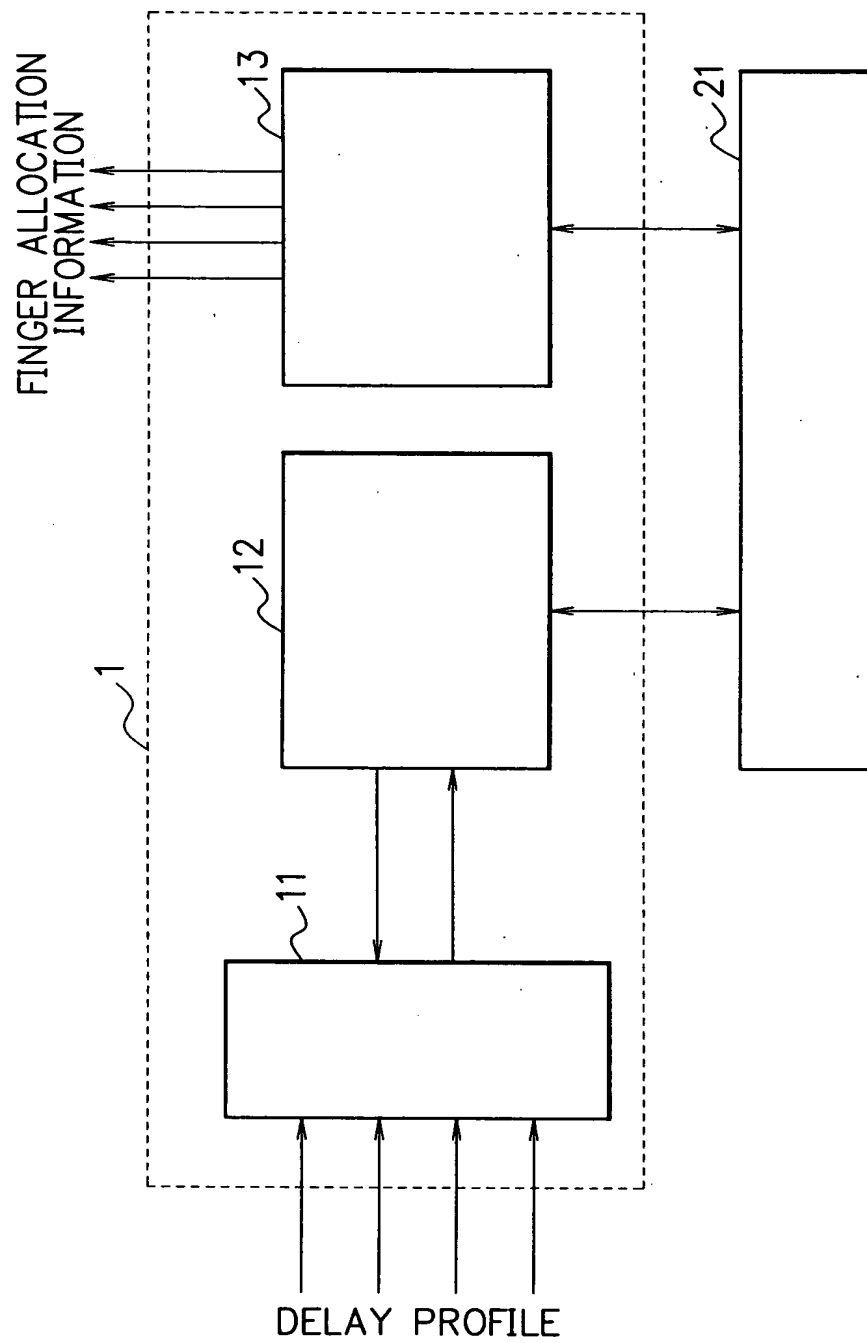


FIG. 3

BRANCH 1

BRANCH 2

BRANCH 3

PATH NO.	SYNCHRONOUS STATE	PATH DELAY TIME	CORRELATION LEVEL	ALLOCATED FINGER NO.
#0	SYNCHRONOUS	128	1034	#1
#1	SYNCHRONOUS	136	789	INVALID
#2	ASYNCHRONOUS	INVALID	INVALID	INVALID
#3	SYNCHRONOUS	106	567	#3
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PATH NO.	SYNCHRONOUS STATE	PATH DELAY TIME	CORRELATION LEVEL	ALLOCATED FINGER NO.
#0	SYNCHRONOUS	124	1034	#2
#1	ASYNCHRONOUS	INVALID	INVALID	INVALID
#2	SYNCHRONOUS	143	789	#0
#3	ASYNCHRONOUS	INVALID	INVALID	INVALID

• • •

FIG. 4

N_BH	N_Path	ACCEPTABLE NUMBER OF PATHS
1	12	12
2	8	16
3	7	21
4	6	24
5	6	30
6	6	36
7	6	42
8	5	40

FIG. 5

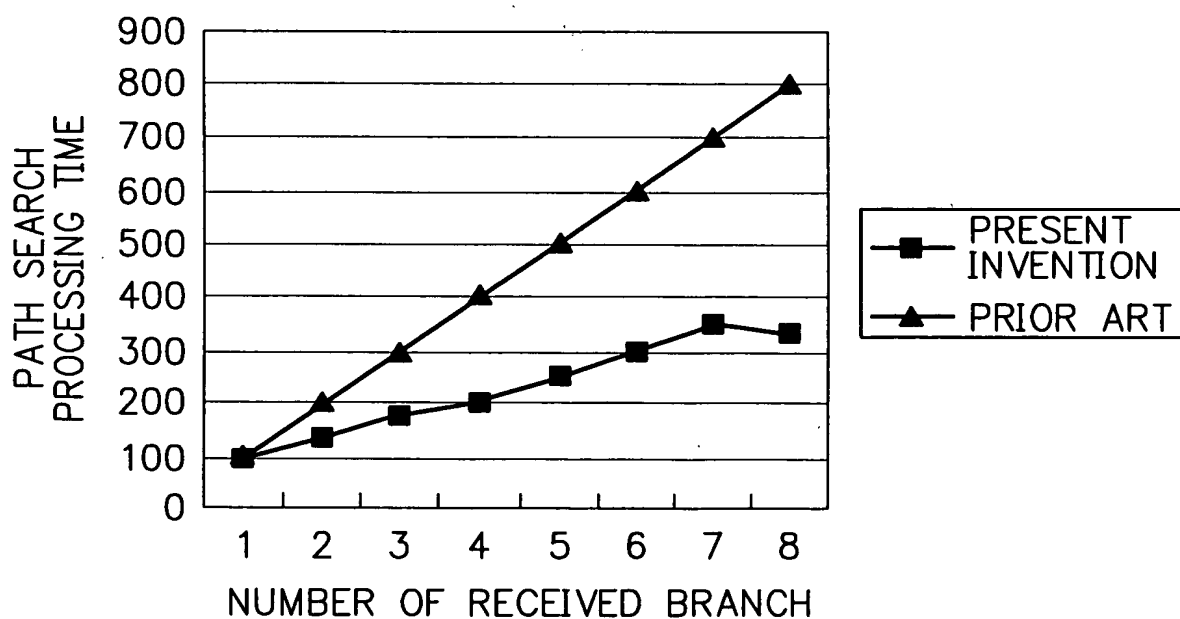


FIG. 6

